

## Electronic Patent Application Fee Transmittal

<b>Application Number:</b>	10717977
<b>Filing Date:</b>	20-Nov-2003
<b>Title of Invention:</b>	Parallel multi-layer printed circuit board having improved interconnection and method for manufacturing the same
<b>First Named Inventor/Applicant Name:</b>	Jee-Soo Mok
<b>Filer:</b>	Laura Cruz/Marivick Watson
<b>Attorney Docket Number:</b>	LEPA122042

Filed as Large Entity

### Utility Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
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**Basic Filing:**

**Pages:**

**Claims:**

**Miscellaneous-Filing:**

**Petition:**

**Patent-Appeals-and-Interference:**

Post-Allowance-and-Post-Issuance:

**Extension-of-Time:**

Extension - 1 month with \$0 paid	1251	1	120	120
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Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
<b>Total in USD (\$)</b>				<b>120</b>